

Foreword from the President of Institute of Semiconductor Engineering

I would like to congratulate on successfully launching the inaugural issue of JSE, the Journal of Semiconductor Engineering.

JSE is the official journal sponsored and published by the Institute of Semiconductor Engineering, ISE.

ISE was established in 2017 to be an academic and professional institute for serving the Korean semiconductor industry matching up to the global presence of Korea in the semiconductor industry and thereby promoting the Korean research leadership in this technology.

Now that the official journal, JSE, is being commenced, I hope JSE will become the supreme gathering place of the leading-edge research on semiconductor engineering and technology in a wide framework of materials, processes, devices, circuits, systems, and design automations including interdisciplinary fields.

I hope the editorial team composed by prominent scholars in the field, led by Prof Seung-Hoon Lee of Sogang University would fully exercise their expertise and vast experiences to bring about the most valuable journal filled with profoundly inspiring, state-of-the-art research in the semiconductor field not only from Korea but also globally.

ISE sincerely appreciates your strong support and active participation. Together we will witness the great success and prosperity of JSE.

President of the Institute of Semiconductor Engineering



Deog-Kyoon Jeong
Seoul National University, Seoul,
Korea

Welcome Message from the Editorial Board

Dear Readers,

On behalf of the editorial board members, it is our great pleasure to introduce this inaugural issue of the Journal of Semiconductor Engineering (JSE), an international peer-reviewed open-access journal published by the Institute of Semiconductor Engineers. The field of semiconductor research and engineering is evolving at an ever increasing pace. We have created the JSE to provide a collective and open forum where semiconductor engineers, students, and scholars all over the world can share and discuss pioneering ideas, research results, and comments with colleagues. The scope of JSE covers all areas of semiconductor materials, process, devices, circuits and systems. Original research, reviews, and critical commentaries will be welcomed.

Because the JSE is 100% open access, all content is available without charge to readers. All JSE readers are allowed to read, download, and print the full texts of the articles. Open access is provided through the payment of an article processing charge (APC) paid by authors or their institutions after acceptance.

We, the editorial board members, are proud to become a member of this new international peer-reviewed open-access journal because we strongly believe that the JSE will provide our valuable readers with opportunities to broaden the new horizon of semiconductor technologies and narrow the gap between the academia and industry.

Once again, we welcome you to the JSE and hope you to join us as readers and contributors.

With warm regards,

[Editor-in-chief]



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